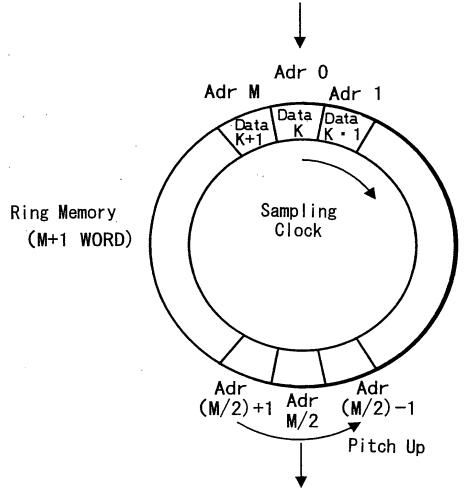


F1G. 2

Write-Address Position (Fixed)



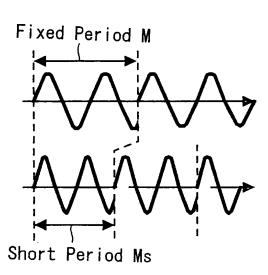
Read-Address Position (Variable)

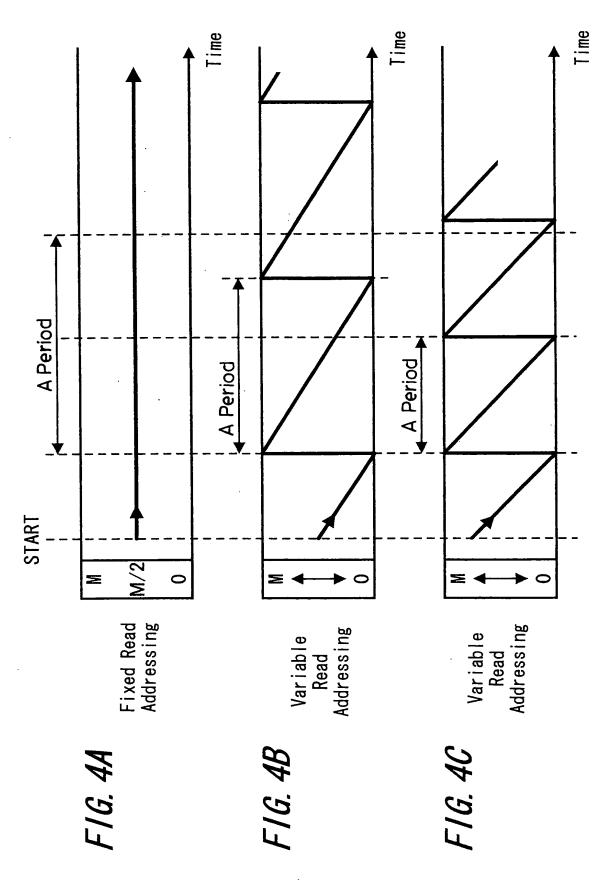
FIG. 3A

Fixed Output

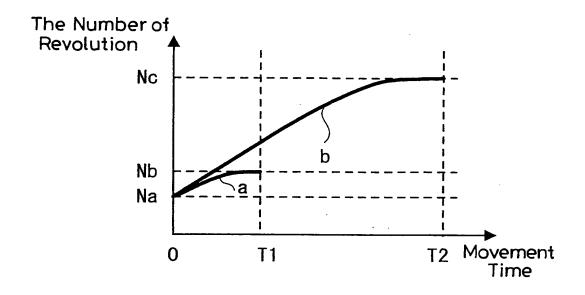
FIG. 3B

Variable Output

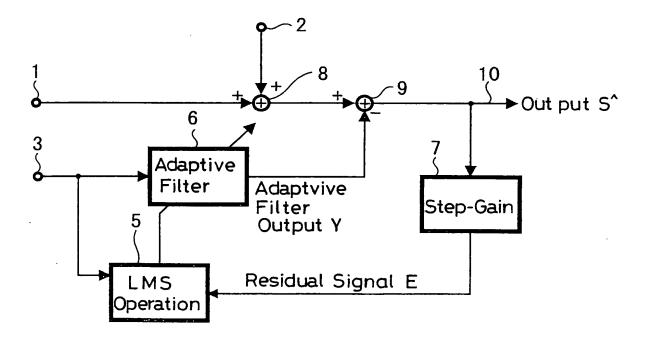


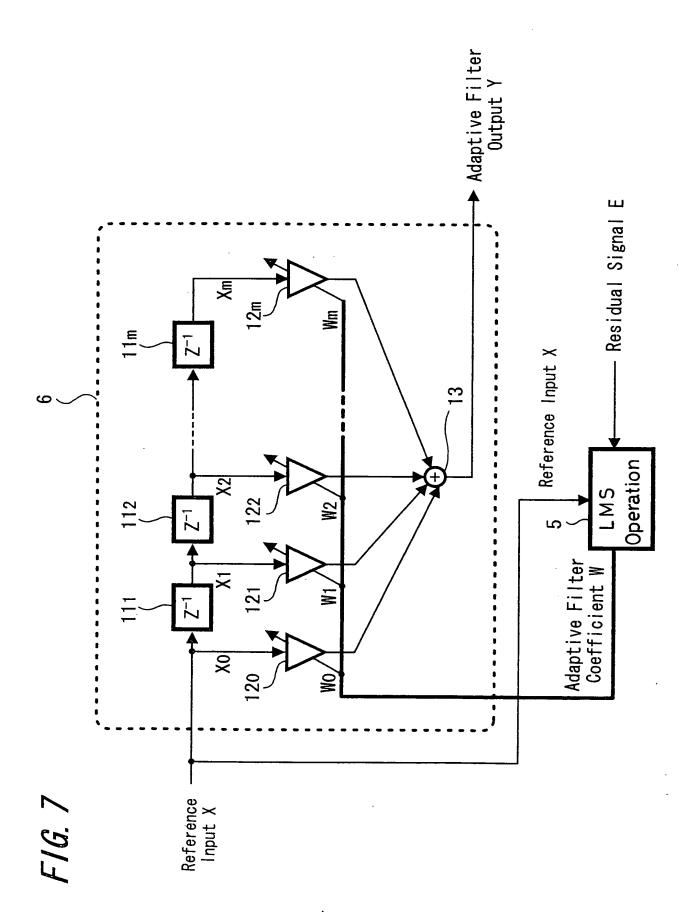


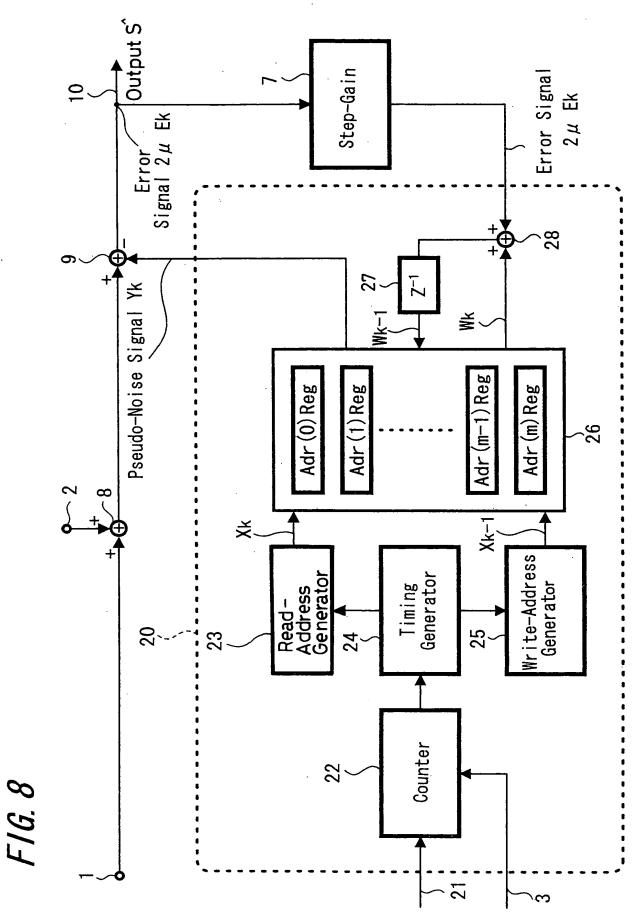
F/G. 5



F/G. 6







F/G. 9

Read-Address Position (Fixed)

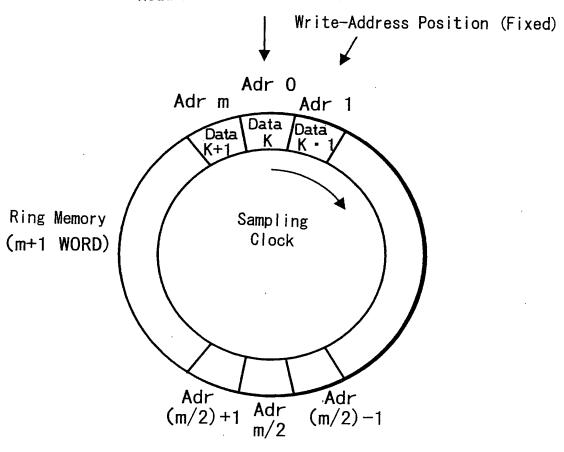
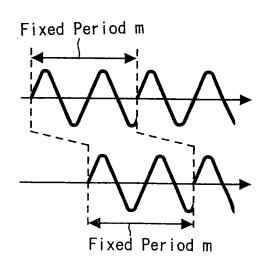


FIG. 10A

Write Signal

FIG. 10B

Read Signal



DESCRIPTION OF REFERENCE NUMERALS

- 1 INPUT TERMINAL TO WHICH MAIN AUDIO INPUT S IS SUPPLIED
- 2 INPUT TERMINAL TO WHICH NOISE N IS SUPPLIED
- 3 ... INPUT TERMINAL TO WHICH VARIABLE PERIOD PULSE SIGNAL IS
 SUPPLIED AS REFERENCE INPUT SIGNAL
- 7 STEP GAIN
- 8, 9 ... ADDER
- 10 ... OUTPUT TERMINAL
- 30 ... ADAPTIVE SIGNAL PROCESSOR
- 31 ... INPUT TERMINAL TO WHICH SAMPLING SIGNAL IS SUPPLIED
- 32 ... COUNTER
- 33 ... VARIABLE READ ADDRESS GENERATOR
- 34 ... TIMING GENERATOR
- 35 ... WRITE ADDRESS GENERATOR
- 36 ... ACCUMULATOR
- 37 ... UNIT SAMPLE TIME DELAY
- 38 ... ADDER
- 39 ... DATA INTERPOLATION MEANS